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| **ID** | **Counter** | **Register** | **Memory and Programmable Logic** |
| 180041101 | Binary Ripple Counter | Register with Parallel Load | RAM read and write operation |
| 180041102 | BCD Ripple Counter | Shift Register | RAM timing waveforms (read and write cycle) |
| 180041103 | Modulus Ripple Counter | Universal Shift Register | types of memory |
| 180041104 | Binary Synchronous Counter | SISO register | Internal construction of memory cell |
| 180041105 | Up Down Ripple Counter | SIPO register | Coincident Decoding |
| 180041106 | Up Down Synchronous Counter | PISO register | Address Multiplexing |
| 180041107 | BCD Synchronous Counter | PIPO register | Hamming Code |
| 180041108 | Ring Counter | Bidirectional Shift Register | Single Error Correction |
| 180041109 | Counter with Unused States | Register with Parallel Load | Double Error Correction |
| 180041110 | Johnson Counter | Shift Register | Introduction ROM with Decoder |
| 180041111 | Comparison between Ripple and Synchronous counter | Universal Shift Register | ROM: Combinational Circuit Implementation |
| 180041112 | Binary Ripple Counter | SISO register | Types of ROM |
| 180041113 | BCD Ripple Counter | SIPO register | Programmable Logic Array |
| 180041114 | Modulus Ripple Counter | PISO register | Programmable Array Logic |
| 180041115 | Binary Synchronous Counter | PIPO register | RAM read and write operation |
| 180041116 | Up Down Ripple Counter | Bidirectional Shift Register | RAM timing waveforms (read and write cycle) |
| 180041117 | Up Down Synchronous Counter | Register with Parallel Load | types of memory |
| 180041118 | BCD Synchronous Counter | Shift Register | Internal construction of memory cell |
| 180041119 | Ring Counter | Universal Shift Register | Coincident Decoding |
| 180041120 | Counter with Unused States | SISO register | Address Multiplexing |
| 180041121 | Johnson Counter | SIPO register | Hamming Code |
| 180041122 | Comparison between Ripple and Synchronous counter | PISO register | Single Error Correction |
| 180041123 | Binary Ripple Counter | PIPO register | Double Error Correction |
| 180041124 | BCD Ripple Counter | Bidirectional Shift Register | Introduction ROM with Decoder |
| 180041125 | Modulus Ripple Counter | Register with Parallel Load | ROM: Combinational Circuit Implementation |
| 180041126 | Binary Synchronous Counter | Shift Register | Types of ROM |
| 180041127 | Up Down Ripple Counter | Universal Shift Register | Programmable Logic Array |
| 180041128 | Up Down Synchronous Counter | SISO register | Programmable Array Logic |
| 180041129 | BCD Synchronous Counter | SIPO register | RAM read and write operation |
| 180041130 | Ring Counter | PISO register | RAM timing waveforms (read and write cycle) |
| 180041131 | Counter with Unused States | PIPO register | types of memory |
| 180041132 | Johnson Counter | Bidirectional Shift Register | Internal construction of memory cell |
| 180041133 | Comparison between Ripple and Synchronous counter | Register with Parallel Load | Coincident Decoding |
| 180041134 | Binary Ripple Counter | Shift Register | Address Multiplexing |
| 180041135 | BCD Ripple Counter | Universal Shift Register | Hamming Code |
| 180041136 | Modulus Ripple Counter | SISO register | Single Error Correction |
| 180041137 | Binary Synchronous Counter | SIPO register | Double Error Correction |
| 180041138 | Up Down Ripple Counter | PISO register | Introduction ROM with Decoder |
| 180041139 | Up Down Synchronous Counter | PIPO register | ROM: Combinational Circuit Implementation |
| 180041140 | BCD Synchronous Counter | Bidirectional Shift Register | Types of ROM |
| 180041141 | Ring Counter | Register with Parallel Load | Programmable Logic Array |
| 180041142 | Counter with Unused States | Shift Register | Programmable Array Logic |
| 180041143 | Johnson Counter | Universal Shift Register | RAM read and write operation |
| 180041144 | Comparison between Ripple and Synchronous counter | SISO register | RAM timing waveforms (read and write cycle) |
| 180041145 | Binary Ripple Counter | SIPO register | types of memory |
| 180041146 | BCD Ripple Counter | PISO register | Internal construction of memory cell |
| 180041147 | Modulus Ripple Counter | PIPO register | Coincident Decoding |
| 180041148 | Binary Synchronous Counter | Bidirectional Shift Register | Address Multiplexing |
| 180041149 | Up Down Ripple Counter | Register with Parallel Load | Hamming Code |
| 180041150 | Up Down Synchronous Counter | Shift Register | Single Error Correction |
| 180041151 | BCD Synchronous Counter | Universal Shift Register | Double Error Correction |
| 180041152 | Ring Counter | SISO register | Introduction ROM with Decoder |
| 180041153 | Counter with Unused States | SIPO register | ROM: Combinational Circuit Implementation |
| 180041154 | Johnson Counter | PISO register | Types of ROM |
| 180041155 | Comparison between Ripple and Synchronous counter | PIPO register | Programmable Logic Array |
| 180041156 | Binary Ripple Counter | Bidirectional Shift Register | Programmable Array Logic |
| 180041157 | BCD Ripple Counter | Register with Parallel Load | RAM read and write operation |
| 180041158 | Modulus Ripple Counter | Shift Register | RAM timing waveforms (read and write cycle) |
| 180041159 | Binary Synchronous Counter | Universal Shift Register | types of memory |
| 180041160 | Up Down Ripple Counter | SISO register | Internal construction of memory cell |
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| 180041201 | Ring Counter | PISO register | Hamming Code |
| 180041202 | Counter with Unused States | PIPO register | Single Error Correction |
| 180041203 | Johnson Counter | Bidirectional Shift Register | Double Error Correction |
| 180041204 | Comparison between Ripple and Synchronous counter | Register with Parallel Load | Introduction ROM with Decoder |
| 180041205 | Up Down Synchronous Counter | Shift Register | ROM: Combinational Circuit Implementation |
| 180041206 | BCD Synchronous Counter | Universal Shift Register | Types of ROM |
| 180041207 | Binary Ripple Counter | SISO register | Programmable Logic Array |
| 180041208 | BCD Ripple Counter | SIPO register | Programmable Array Logic |
| 180041209 | Modulus Ripple Counter | PISO register | RAM read and write operation |
| 180041210 | Binary Synchronous Counter | PIPO register | RAM timing waveforms (read and write cycle) |
| 180041211 | Up Down Ripple Counter | Bidirectional Shift Register | types of memory |
| 180041212 | Up Down Synchronous Counter | Register with Parallel Load | Internal construction of memory cell |
| 180041213 | BCD Synchronous Counter | Shift Register | Coincident Decoding |
| 180041214 | Ring Counter | Universal Shift Register | Address Multiplexing |
| 180041215 | Counter with Unused States | SISO register | Hamming Code |
| 180041216 | Johnson Counter | SIPO register | Single Error Correction |
| 180041217 | Comparison between Ripple and Synchronous counter | PISO register | Double Error Correction |
| 180041218 | Binary Ripple Counter | PIPO register | Introduction ROM with Decoder |
| 180041219 | BCD Ripple Counter | Bidirectional Shift Register | ROM: Combinational Circuit Implementation |
| 180041220 | Modulus Ripple Counter | Register with Parallel Load | Types of ROM |
| 180041221 | Binary Synchronous Counter | Shift Register | Programmable Logic Array |
| 180041222 | Up Down Ripple Counter | Universal Shift Register | Programmable Array Logic |
| 180041223 | Up Down Synchronous Counter | SISO register | RAM read and write operation |
| 180041224 | BCD Synchronous Counter | SIPO register | RAM timing waveforms (read and write cycle) |
| 180041225 | Ring Counter | PISO register | types of memory |
| 180041226 | Counter with Unused States | PIPO register | Internal construction of memory cell |
| 180041227 | Johnson Counter | Bidirectional Shift Register | Coincident Decoding |
| 180041228 | Comparison between Ripple and Synchronous counter | Register with Parallel Load | Address Multiplexing |
| 180041229 | Binary Ripple Counter | Shift Register | Hamming Code |
| 180041230 | BCD Ripple Counter | Universal Shift Register | Single Error Correction |
| 180041231 | Modulus Ripple Counter | SISO register | Double Error Correction |
| 180041232 | Binary Synchronous Counter | SIPO register | Introduction ROM with Decoder |
| 180041233 | Up Down Ripple Counter | PISO register | ROM: Combinational Circuit Implementation |
| 180041234 | Up Down Synchronous Counter | PIPO register | Types of ROM |
| 180041235 | BCD Synchronous Counter | Bidirectional Shift Register | Programmable Logic Array |
| 180041236 | Ring Counter | Register with Parallel Load | Programmable Array Logic |
| 180041237 | Counter with Unused States | Shift Register | RAM read and write operation |
| 180041238 | Johnson Counter | Universal Shift Register | RAM timing waveforms (read and write cycle) |
| 180041239 | Comparison between Ripple and Synchronous counter | SISO register | types of memory |
| 180041240 | Binary Ripple Counter | SIPO register | Internal construction of memory cell |
| 180041241 | BCD Ripple Counter | PISO register | Coincident Decoding |
| 180041242 | Modulus Ripple Counter | PIPO register | Address Multiplexing |
| 180041243 | Binary Synchronous Counter | Bidirectional Shift Register | Hamming Code |
| 180041244 | Up Down Ripple Counter | Register with Parallel Load | Single Error Correction |
| 180041245 | Up Down Synchronous Counter | Shift Register | Double Error Correction |
| 180041246 | BCD Synchronous Counter | Universal Shift Register | Introduction ROM with Decoder |
| 180041247 | Ring Counter | SISO register | ROM: Combinational Circuit Implementation |
| 180041248 | Counter with Unused States | SIPO register | Types of ROM |
| 180041249 | Johnson Counter | PISO register | Programmable Logic Array |
| 180041250 | Comparison between Ripple and Synchronous counter | PIPO register | Programmable Array Logic |
| 180041251 | Binary Ripple Counter | Bidirectional Shift Register | RAM read and write operation |
| 180041252 | BCD Ripple Counter | Register with Parallel Load | RAM timing waveforms (read and write cycle) |
| 180041253 | Modulus Ripple Counter | Shift Register | types of memory |
| 180041254 | Binary Synchronous Counter | Universal Shift Register | Internal construction of memory cell |
| 180041255 | Up Down Ripple Counter | SISO register | Coincident Decoding |
| 180041256 | Up Down Synchronous Counter | SIPO register | Address Multiplexing |
| 180041257 | BCD Synchronous Counter | PISO register | Hamming Code |
| 180041258 | Ring Counter | PIPO register | Single Error Correction |
| 180041259 | Counter with Unused States | Bidirectional Shift Register | Double Error Correction |
| 180041260 | Johnson Counter | Universal Shift Register | Introduction ROM with Decoder |